

**Typical Features**

- ◆ Fixed input voltage, isolated & regulated output power 1W
- ◆ Efficiency up to 78%
- ◆ Mini size SIP package
- ◆ Isolation voltage 1500VDC
- ◆ Operating temperature from -40°C to +85°C
- ◆ Plastic case, flame class UL94-V0



**Test Condition:** Unless otherwise specified, all parameter values had been tested at nominal input voltage, pure resistive rated load, and at room temperature 25°C.

**Application Field**

*This series of products can be widely used in the fields of instrument, communication, pure digital circuit, general low frequency analog circuit, relay drive circuit, data exchange circuit, etc.*

**Typical Product List**

Certificate	Part No.	Input Voltage Range (VDC)		Output Voltage/Current (Vo/Io)		Input Current (mA) Typ. @Nominal Voltage		Max. Capacitive Load	Ripple & Noise (20MHz) (mVp-p) Max/Typ.	Efficiency (%) @full load/nom. voltage	
		Nom.	Range	Vo (VDC)	Io(mA) Max	Full load	No Load			Min	Typ.
-	FW1-3V3S15B	3.3	3.135 - 3.465	15	67	400	15	560	100/50	68	70
-	FW1-05S3V3B	5	4.75 - 5.25	3.3	250	280	10	2400	100/50	64	66
CE	FW1-05S05B			5	200	279	10	2400	100/50	70	72
-	FW1-05S12B			12	83	256	15	560	100/50	76	78
-	FW1-05S15B			15	67	256	18	560	100/50	75	77
-	FW1-05S24B			24	42	276	18	220	100/50	71	73
CE	FW1-12S05B	12	11.4 - 12.6	5	200	109	10	2400	100/50	75	77
-	FW1-12S09B			9	111	110	12	1000	100/50	75	77
-	FW1-12S12B			12	83	107	10	560	100/50	75	77
-	FW1-12S15B			15	67	103	10	560	100/50	72	74
-	FW1-12S24B			24	42	124	10	220	100/50	65	67
-	FW1-15S05B	15	14.25 - 15.75	5	200	94	10	2400	100/50	68	70
-	FW1-15S15B			15	67	85	11	560	100/50	76	78
-	FW1-24S3V3B	24	22.8 - 25.2	3.3	250	60	10	2400	100/50	67	69
CE	FW1-24S05B			5	200	57	10	2400	100/50	72	74
-	FW1-24S12B			12	83	54	10	560	100/50	74	76
-	FW1-24S15B			15	67	53	10	560	100/50	75	77
-	FW1-24S24B			24	42	63	10	220	100/50	65	67

Note: The Ripple & Noise is tested by the twisted pair method.

**Input Specifications**

Item	Operating conditions	Min.	Typ.	Max.	Unit	
Input Inrush Voltage (1Sec. Max)	3.3Vdc input	-0.7	--	7	VDC	
	5Vdc input	-0.7	--	9		
	9Vdc input	-0.7	--	12		
	12Vdc input	-0.7	--	18		
	15Vdc input	-0.7	--	21		
	24Vdc input	-0.7	--	30		
Input Filter	Capacitor Filter					
Hot Plug	Unavailable					

**Output Specifications**

Item	Operating conditions	Min.	Typ.	Max.	Unit
Output Power		0.1	--	1	W
Output Voltage Accuracy	Nominal input voltage, full load	--	±2	±3	%
Load Regulation	10% ~ 100% load	--	--	±3	
Line Regulation	Input voltage change ±1%	--	--	±0.25	--
Temperature Drift Coefficient	100% Load	--	--	±0.03	%/°C
Short Circuit Protection	Continuous, self-recovery				

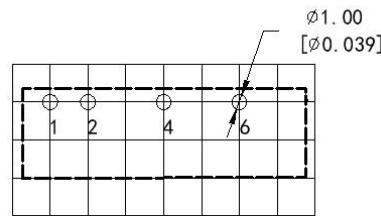
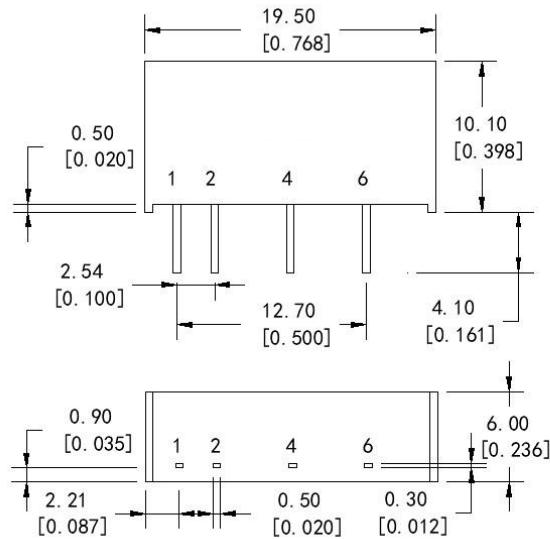
**General Specifications**

Item	Operating conditions	Min.	Typ.	Max.	Unit
Switching Frequency	Nominal input voltage, full load	--	260	--	KHz
Operating Temperature	Refer to the temperature derating graph	-40	--	+85	°C
Storage Temperature		-55	--	+125	
Case temperature rise	Operating at Ta =25°C	--	30°	--	
Pin Soldering Temperature	1.5mm from the case, 10S	--	--	300	
Relative humidity	No condensation	5	--	95	%RH
Isolation Voltage	I/P – O/P, test 1min, leakage current <1mA	1500	--	--	VDC
Insulation Resistance	I/P – O/P, @ 500VDC	1000	--	--	MΩ
Isolation Capacitance	I/P – O/P,100KHz/0.1V	--	20	--	pF
Vibration		10-150Hz, 5G, 30 Min. along X, Y and Z			
MTBF	MIL-HDBK-217F@25°C	3500	--	--	K hours
Case Material	Plastic in Black, flame class UL94-V0				
Unit Weight	2.1g (Typ.)				
Cooling Method	Natural air				

Packing	Tube size (525x18x10 mm)	25PCS/Tube	
	Carton size (542x110x155 mm)	2000PCS (Total 80 Tubes)	
Unit Dimensions	L x W x H	19.50×6.00×10.10 mm	0.768×0.236×0.398 inch

**EMC Performance**

EMI	CE	CISPR32/EN55032 CLASS B (With the recommended EMC circuit)
	RE	CISPR32/EN55032 CLASS B (With the recommended EMC circuit)
EMS	ESD	IEC/EN61000-4-2 Contact ±4KV, air ±8KV perf.Criteria B

**Mechanical Dimensions**

PCB layout vertical view  
Grid 2.54x2.54[0.10x0.10]

Unit: mm[inch]  
Pin section tolerance: ±0.10[±0.004]  
General tolerance: ±0.50[±0.020]

**Pin-out Function Description**

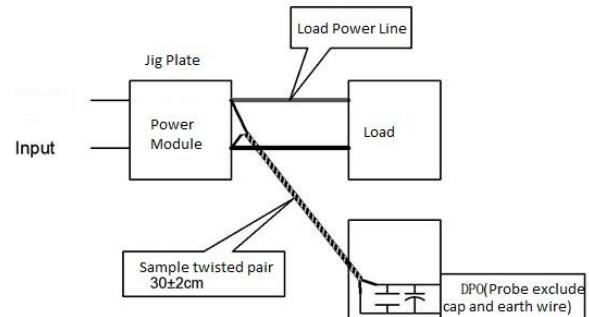
Pin No.	1	2	3	4	5	6
Single (S)	+Vin	GND	No Pin	-Vo	No Pin	+Vo

Note: Please take the pin function definition on the product label as the right one if it is different than the data sheet description.

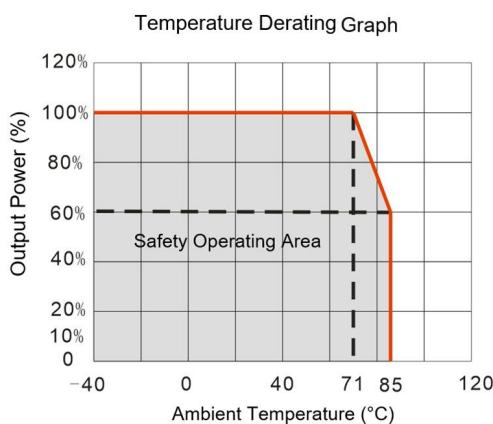
**Ripple & Noise Test Instruction (Twisted Pair Method, 20MHz Bandwidth)**

1) The Ripple & noise test needs 12# twisted pair cables, an oscilloscope which bandwidth should be set to 20MHz, 0.1uF polypropylene capacitor and 10uF high-frequency low-resistance electrolytic capacitor are connected in parallel with the probes (100M bandwidth). The oscilloscope should be set at the Sample Mode.

2) The test diagram is shown on the right. The converter output connects to the electronic load by the jig with cables which size should be defined according to the output current value. The twisted pair (length 30cm±2 cm) should be connected in parallel with the load, the location is as close as possible to the output pins or terminals. The test can be start after input power on.



## Product Characteristics Graph



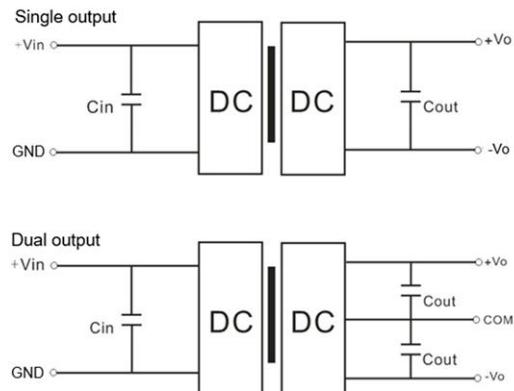
## Recommended Circuits for Application

## 1. Requirement for Output load

The maximum capacitive load of the product was tested at the Rated full load. The converter may not start or be damaged if the output capacitor exceeds this value.

## 2. Typical application circuits

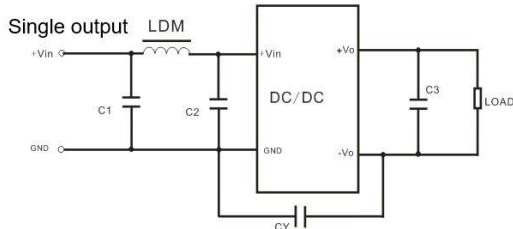
To effectively decrease the input and output ripple and noise, a capacitor filter can be connected at the input and output as the application circuit shown in the figure below. The suitable filtering capacitors should be chosen as the recommended capacitive load values in Table 1. The converter could not start if the capacitance is too big.



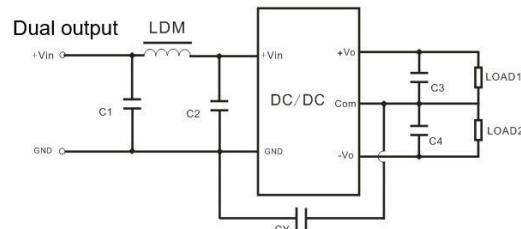
Recommended Capacitive Load Value Table (Table 1)

Vin (Vdc)	Cin	Single Vout (Vdc)	Cout (μF)	Dual Vout (Vdc)	Cout (μF)
5	10 μF/16V	3. 3	10 μF/16V	±3. 3	4. 7 μF/16V
12	2. 2 μF/25V	5	10 μF/16V	±5	4. 7 μF/16V
15	2. 2 μF/25V	9	2. 2 μF/25V	±9	2. 2 μF/25V
24	1 μF/50V	12	2. 2 μF/25V	±12	1 μF/25V
--	--	15	1 μF/25V	±15	1 μF/25V
--	--	24	1 μF/50V	±24	0. 47 μF/50V

### 3. Recommended EMC Circuit Diagram



	Input voltage	5VDC	12/15/24VDC
EMI	C1/C2	4. 7μF/16V	4. 7μF/50V
	CY	270pF/2KV	270pF/2KV
	C3	Refer to Cout in Table 1	
	LDM	6. 8 μ H	6. 8 μ H



	Input voltage	5VDC	12/15/24VDC
EMI	C1/C2	4. 7μF/16V	4. 7μF/50V
	CY	270pF/2KVdc	270pF/2KVdc
	C3/C4	Refer to Cout in Table 1	
	LDM	6. 8 μ H	6. 8 μ H

### Application Notice

1. This product cannot be used in parallel connected, and it does not support hot-plug.
2. The product performance in this manual cannot be guaranteed if it works at a lower load than the minimum load condition.
3. All values or indicators in this manual have been tested based on Aipupower test specifications.

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